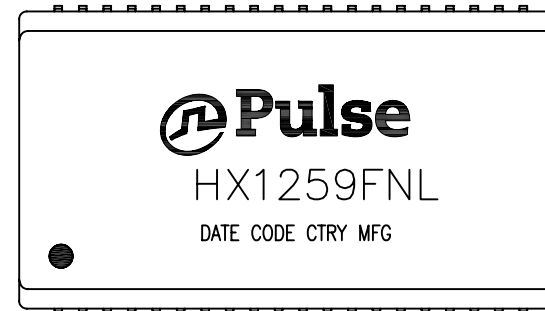


NOTES:

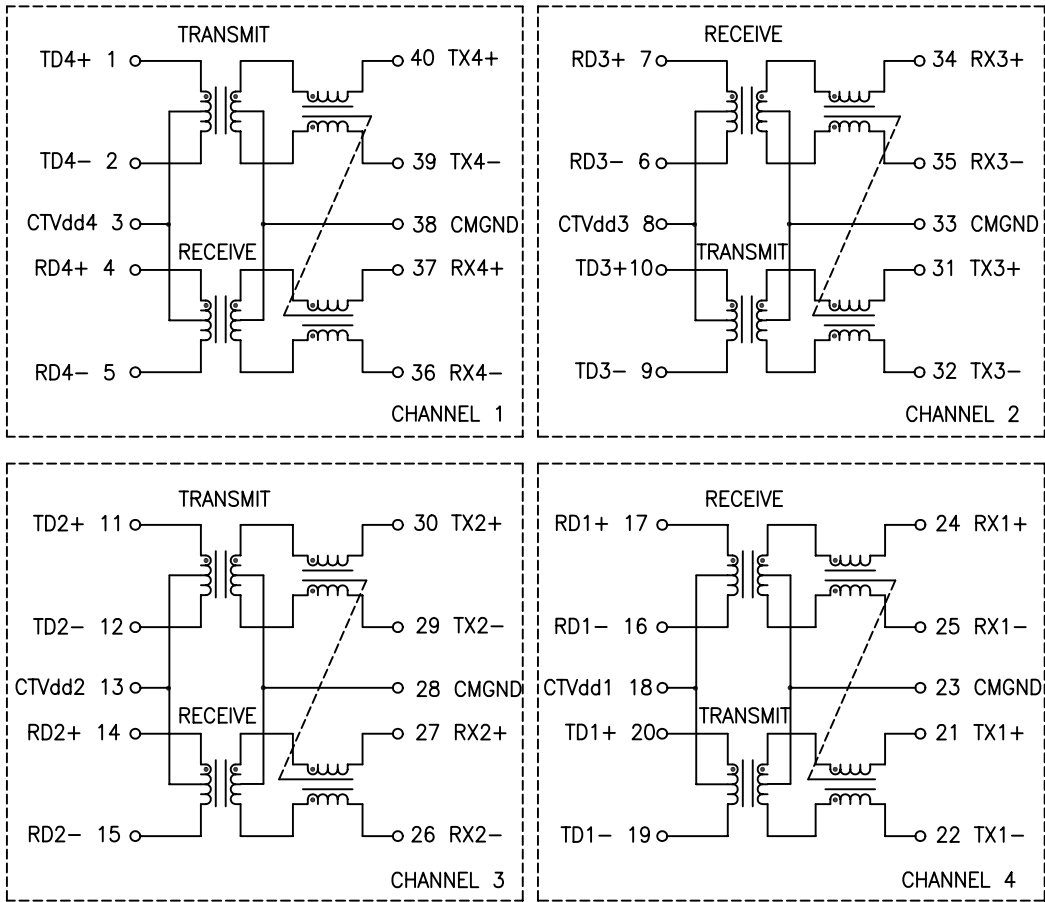
1. ROHS COMPLIANT
2. HEADER: PHENOLIC WITH FLAMMABILITY RATING UL 94V-0 OR BETTER.
3. STORAGE TEMPERATURE: -50°C TO +125°C
4. COMPLIANCE TO J-STD:
 - A. J-STD-002: SOLDERABILITY AT 245°C REFLOW PROFILE
 - B. J-STD-020: LEVEL 1, NO MOISTURE SENSITIVE
 - C. J-STD-075: R7, 245°C MAXIMUM THROUGH REFLOW SOLDER
5. TO ORDER TAPE & REEL PACKAGING ADD A "T" SUFFIX TO THE PART NUMBER(i.e HX1259FNL BECOMES HX1259FNLT).



FINAL OUTLINE

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PRODUCT DESCRIPTION	TLA DRAWING	PS DRAWING	SHEET	PART NO.	DATASHEET REV.
MDL,QUAD,100D,1:1,SM,TU	HX1259FNL-10	PS-2743.001-A	1 OF 3	HX1259FNL	A



SCHEMATIC

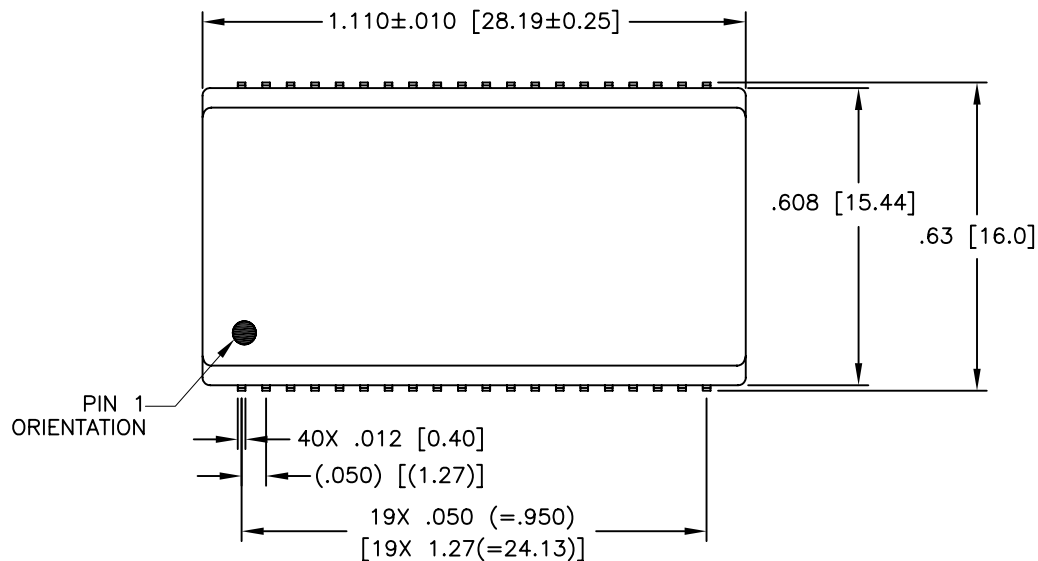
ELECTRICAL CHARACTERISTICS AT +25°C UNLESS OTHER SPECIFIED
MEETS IEEE 802.3 SPECIFICATION

PARAMETER	SPECIFICATIONS		
OPERATING TEMPERATURE	-40°C ~ +85°C		
URNS RATIO	1 : 1 ±2%		
POLARITY	PER SCHEMATIC		
INSERTION LOSS	100 KHz	1-125 MHz	
	-1.2 dB MAX	-0.2-0.002*f ^{1.4} dB MAX	
RETURN LOSS, 100BT (Z OUT = 100 OHM ±15%)	.1-30 MHz	30-60 MHz	60-80 MHz
	-16 dB MIN	-10+20*LOG ₁₀ (f/60 MHz) dB MIN	-10 dB MIN
		(MEASURED AT 100 KHz, 100 mVRMS AND WITH 8 mA DC BIAS)	
INDUCTANCE (OCL) (MEDIA SIDE, -40°C ~ +85°C)	350 uH MIN		
	1 MHz	10-100 MHz	
CROSSTALK, ADJACENT CHANNELS	-50 dB MIN	-55+22*LOG ₁₀ (f/10) dB MIN	
	2 MHz	30-200 MHz	
COMMON MODE REJECTION RATIO	-50 dB MIN	-15+20*LOG ₁₀ (f/200) dB MIN	
	.65 OHMS MAX		
DC RESISTANCE, 1/2 WINDING	±.065 OHMS MAX (CENTER TAP SYMMETRY)		
INPUT - OUTPUT ISOLATION	1500 VRMS MIN @ 60 SECONDS		

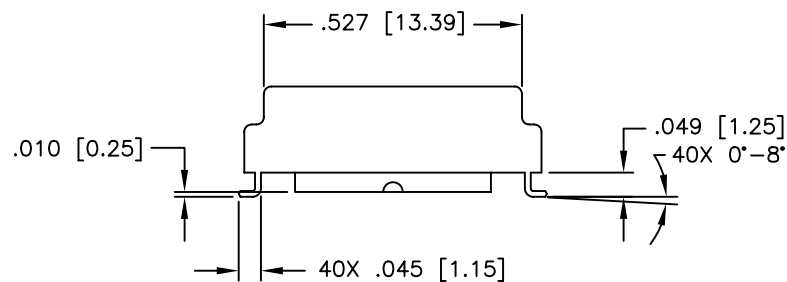
NOTE: f IS FREQUENCY IN MHZ.

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PRODUCT DESCRIPTION	TLA DRAWING	PS DRAWING	SHEET	PART NO.	DATASHEET REV.
MDL,QUAD,100D,1:1,SM,TU	HX1259FNL-10	PS-2743.001-A	2 OF 3	HX1259FNL	A



SUGGESTED PAD LAYOUT



DIMENSIONS ARE IN INCHES [MILLIMETERS] WITH THE FOLLOWING TOLERANCES: [MILLIMETERS] ARE FOR REFERENCE ONLY.
 .XX= ±.01 [±0.25]
 .XXX= ±.005 [±0.13]

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PRODUCT DESCRIPTION	TLA DRAWING	PS DRAWING	SHEET	PART NO.	DATASHEET REV.
MDL,QUAD,100D,1:1,SM,TU	HX1259FNL-10	PS-2743.001-A	3 OF 3	HX1259FNL	A