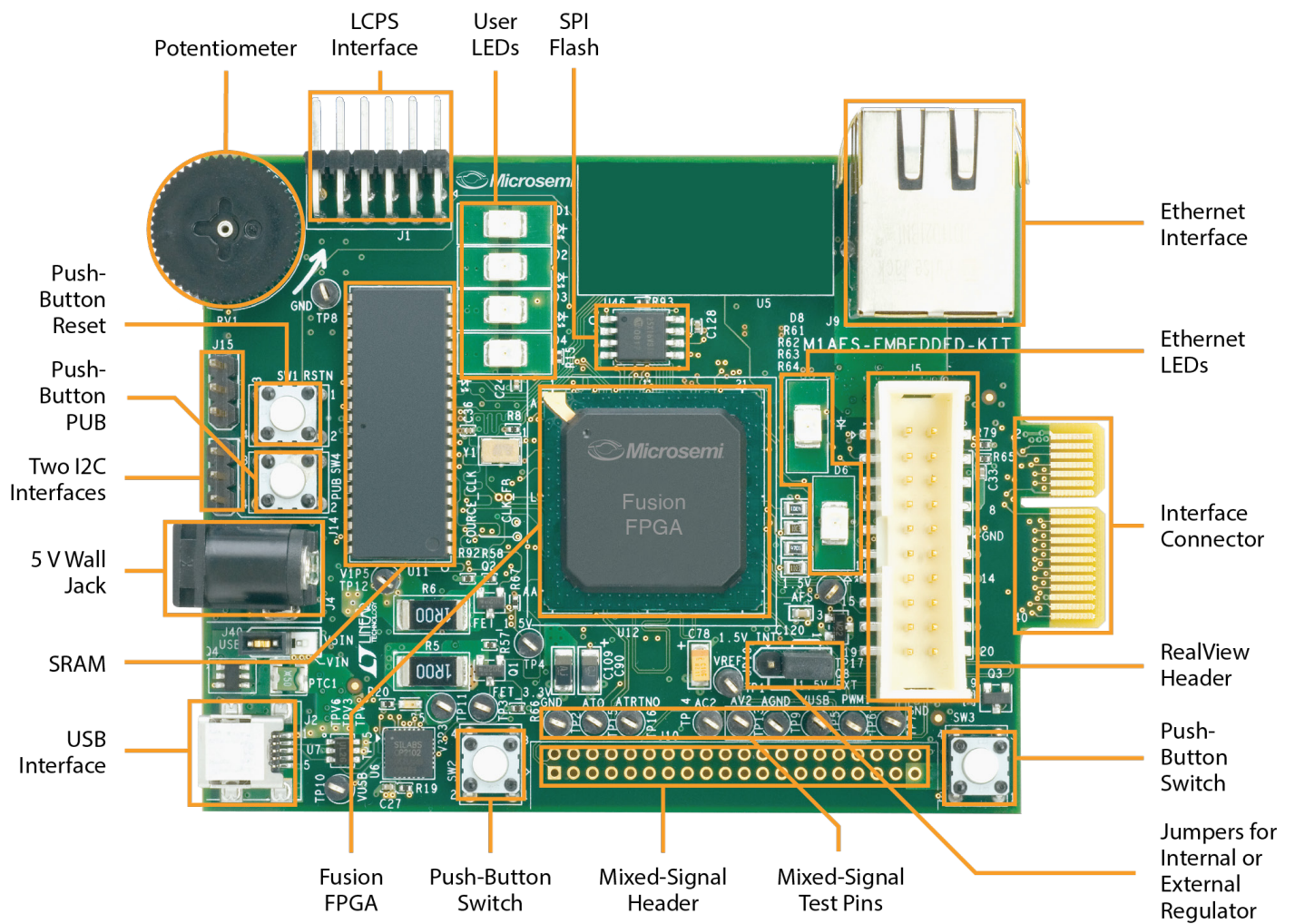


Fusion Embedded Development Kit Quickstart Card

Kit Contents

Quantity	Description
1	Fusion Embedded Development Board with an M1AFS1500-FGG484 device
1	Low-cost programming stick (LCPS)
2	USB 2.0 A to mini-B cable
1	5 V power pack and modular plugs
1	Quickstart card



Overview

The Microsemi Fusion Embedded Development Kit is an embedded system management platform for evaluating the advanced features of Fusion FPGAs. It can be used for mixed-signal and embedded processor development applications.

The temperature diode, potentiometer, and PWM circuit available on the Fusion Embedded Development Board enable development of mixed-signal applications such as voltage sequencing, voltage trimming, gaming, motor control, temperature monitor, and touchscreen applications. The mixed-signal header on the board allows daughter boards to be attached to the board for extended mixed-signal applications.

The ARM Cortex™-M1-enabled Fusion device in an FGG484 package is placed on the board. The board also has Ethernet and USB-to-UART interfaces for communication with the Fusion FPGA.

To assist in embedded processor development, components such as I2C, EEPROM, SRAM, and SPI flash are available on the board.

The board can be powered using a USB port and includes a programming stick header that allows the LCPS included in the kit to be attached to the board for programming the Fusion M1AFS1500 device.

Jumper Settings

The following table lists the recommended jumper settings for the pre-programmed demo design available on the Fusion Embedded Development Board.

Jumper	Pin	Development Kit Function
JP10	3–2	Jumper to choose between 1.5 V external regulator and Fusion 1.5 V internal regulator. Pin 1–2 for 1.5 V internal regulator. Pin 3–2 for 1.5 V external regulator.
JP40	1–2	Jumper to select the power source. Pin 1–2 for USB. Pin 3–2 for 5 V power brick.

Software and Licensing

Libero® SoC Design Suite offers high productivity with its comprehensive, easy-to-learn, easy-to-adopt development tools for designing with Microsemi's low power Flash FPGAs and SoCs. The suite integrates industry standard Synopsys Synplify Pro® synthesis and Mentor Graphics ModelSim® simulation with best-in-class constraints management and debug capabilities.

Download the latest Libero SoC release

www.microsemi.com/products/fpga-soc/design-resources/design-software/libero-soc#downloads

Generate a Libero Silver license for your kit

www.microsemi.com/products/fpga-soc/design-resources/licensing

Documentation Resources

For more information about the Fusion Embedded Development Kit, including user's guides, tutorials, and design examples, see the documentation at www.microsemi.com/products/fpga-soc/design-resources/dev-kits/fusion/fusion-embedded-development-kit#documents.

Support

Technical support is available online at www.microsemi.com/soc/support and by email at soc_tech@microsemi.com

Microsemi sales offices, including representatives and distributors, are located worldwide. To find your local representative, go to <http://www.microsemi.com/salescontacts>



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